PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: Doerrer et al.

Group Art Unit: 2819

Serial No.: 10/650,493

Examiner: Young, Brian K.

Filed: August 28, 2003

Docket No.: 1406/163

Confirmation No.: 6981

SIGMA-DELTA-MODULATOR

Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

For:

In accordance with 37 C.F.R. 1.56, 1.97, and 1.98, applicants' undersigned attorney brings to the attention of the Patent and Trademark Office the documents listed on the attached Form PTO-1449. Copies of the references as well as Form PTO-1449 are attached hereto. This is not to be construed as a representation that a search has been made or that a reference is relevant merely because cited.

Early passage of the subject application to issue is earnestly solicited.

Respectfully submitted,

JENKINS, WILSON & TAYLOR, P.A.

Date: <u>December 14, 2004</u>

Ву:

Richard E. Jenkins

Registration No. 28,428

REJ/gwc

Enclosures

Customer No: 25297

1406/163

FORMET -1449 U.S. Department of Commerce Patent and Trademark Office					Attorney Docket No. 1406/163		Serial No. 10/650,493
nents Ci	ted by Applicant						
					Applicant(s): Doerrer et al.		
					Filing Date: August 28, 2003		
		U.S. PATE	ENT DO	CUMENTS	3		
**	Document Number	Date	Name		Class	Subclass	Filing date if Appropriate
1.	4,251,803	2/17/1981	Debord et al.				
2.	5,117,234	05/26/1992	Shizawa				
3.	5,550,544	8/27/1996	Sakiyama et al.				
4.	5,805,093	9/08/1998	Heikkilä et al.				
5.	5,896,101	4/20/1999	Melanson			•	
6.	5,907,299	5/25/1999	Green et al.				
7.	5,990,814	11/23/1999	Croman et al.				
8.	6,331,833 B1	12/18/2001	Navias	sky et al.			
9.	6,426,714 B1	07/30/2002	Ruha et al.				
	-	FOREIGN PA	ATENT	DOCUMEN	NTS		
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10.	WO 01/93430	12/06/2001	SE		SPIREA AB		Yes
11.	WO 02/09292	1/31/2002			The Regents of the University of California		Yes
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	12.	European Search Report, June 3, 2004.			
	13.	Benabes et al., "A methodology for designing continuous-time sigma-delta modulators," <u>IEEE European Design and Test Conference</u> , 1997. (ABSTRACT)			
	14.	Cherry et al., "Continuous-Time Delta-Sigma Modulator for High Speed A/D Conversion," Kluwar Academic Publisher, pp. 75-103, 2000.			
	15.	Gao et al, "Excess loop delay effects in continuous-time delta-sigma modulators and the compensation solution," <u>Circuits and Systems</u> , 1997. (ABSTRACT)			
	16.	Hernandez, L., "Continuous-time sigma-delta modulators with reduced timing jitter sensitivity based on time delays," <u>Electronics Letters</u> , July 10, 2003. (ABSTRACT)			
	17.	Oliaei et al., "Jitter Effects in Continuous-Time Modulators with Delayed Return-to-Zero Feedback," Electronics, Circuits and Systems, pp. 351-354, 1998.			
	18.	Ramesh et al, "Sigma delta analog to digital converters with adaptive quantization," <u>Circuits and Systems</u> , 1997. (ABSTRACT)			

EXAMINER	DATE CONSIDERED
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^{*}Examiner Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.